

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shannon V. Davidson et al.
Serial No.: 10/825021
Filing Date: 15 April 2004
Group Art Unit: 2191
Confirmation No.: 7500
Examiner: Ted T. Vo
Title: *System and Method for Topology-Aware Job Scheduling and Backfilling in an HPC Environment*

Mail Stop: RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Response Under 37 C.F.R. § 1.116

In response to the Office Action sent 11 February 2008, Applicants respectfully request the Examiner to reconsider the rejection of the claims in light of the following.